



Electrical Characteristics at $\pm 5V$

$T_A = 25^\circ C$, $V_S = \pm 5V$, $R_f = 1k\Omega$, $R_L = 1k\Omega$ to GND, $G = 2$; unless otherwise noted.

Symbol	Parameter	Conditions	Spice Model	Data Sheet Typ	Units
Frequency Domain Response					
GBWP	-3dB Gain Bandwidth Product	$G = 10$, $V_{OUT} = 0.05V_{pp}$	36.1	35	MHz
UGBW	Unity Gain Bandwidth	$V_{OUT} = 0.05V_{pp}$, $R_f = 0$	53.3	55	MHz
BW_{SS}	-3dB Bandwidth	$V_{OUT} = 0.05V_{pp}$	26.9	25	MHz
BW_{LS}	Large Signal Bandwidth	$V_{OUT} = 2V_{pp}$	3.5	3.6	MHz
Time Domain Response					
t_R , t_F	Rise and Fall Time	$V_{OUT} = 2V$ step; (10% to 90%)	151	125	ns
t_S	Settling Time to 0.1%	$V_{OUT} = 2V$ step	97	80	ns
OS	Overshoot	$V_{OUT} = 2V$ step	0	0.3	%
SR	Slew Rate	4V step	12	12	V/ μs
Distortion/Noise Response					
HD2	2nd Harmonic Distortion	$2V_{pp}$, 10kHz, $R_L = 1k\Omega$	-131	-125	dBc
		$2V_{pp}$, 100kHz, $R_L = 100\Omega$	-111	-90	dBc
HD3	3rd Harmonic Distortion	$2V_{pp}$, 10kHz, $R_L = 1k\Omega$	-138	-127	dBc
		$2V_{pp}$, 100kHz, $R_L = 100\Omega$	-108	-85	dBc
THD	Total Harmonic Distortion	$1V_{pp}$, 1kHz, $G=1$, $R_L = 2k\Omega$	0.0005	0.00005	%
e_n	Input Voltage Noise	> 10kHz	N/A	5.3	nV/ \sqrt{Hz}
		> 100kHz	3.44	3.5	nV/ \sqrt{Hz}
DC Performance					
V_{IO}	Input Offset Voltage ⁽¹⁾		-0.0498	0.050	mV
dV_{IO}	Average Drift		1.35	1.3	$\mu V/^\circ C$
I_b	Input Bias Current ⁽¹⁾		0.3	-0.30	μA
dI_b	Average Drift		0.85	0.85	nA/ $^\circ C$
I_{os}	Input Offset Current ⁽¹⁾		0.2	0.2	μA
PSRR	Power Supply Rejection Ratio ⁽¹⁾	DC	100	100	dB
A_{OL}	Open-Loop Gain ⁽¹⁾	$V_{OUT} = V_S / 2$	115	115	dB
I_S	Supply Current ⁽¹⁾	per channel	2.19	2.2	mA
Input Characteristics					
R_{IN}	Input Resistance	Non-inverting, $G = 1$	verified	30	M Ω
C_{IN}	Input Capacitance		verified	1	pF
CMIR	Common Mode Input Range		verified	± 5.5	V
CMRR	Common Mode Rejection Ratio ⁽¹⁾	DC, $V_{cm} = -3V$ to $3V$	117	95	dB
Output Characteristics					
V_{OUT}	Output Voltage Swing	$R_L = 150\Omega$	-4.73 to 4.46	-4.826 to 4.534	V
		$R_L = 1k\Omega$ ⁽¹⁾	-4.89 to 4.8	-4.93 to 4.85	V
I_{OUT}	Output Current		+76, -54	+80, -55	mA
I_{SC}	Short-Circuit Output Current	$V_{OUT} = V_S / 2$	+80, -55	+115, -90	mA

Notes: